



PATENT ABSTRACTS OF JAPAN

(11) Publication number: **06315198 A**(43) Date of publication of application: **08.11.94**

(51) Int. Cl.

H04S 1/00**G10K 15/12****H04R 3/04****H04R 3/12****H04S 7/00**(21) Application number: **05102107**(22) Date of filing: **28.04.93**(71) Applicant: **SHARP CORP**(72) Inventor: **SHIROSAKO KATSUNORI
YOSHIIISA KIOHARU****(54) VOICE OUTPUTTING CIRCUIT****(57) Abstract:**

PURPOSE: To provide a voice outputting circuit in which an always optimal stereo effect can be obtained even when a listener listens to an acoustic equipment or a video equipment or the like at any front and back position.

CONSTITUTION: This circuit is equipped with attenuators 7R and 7L for right and left channel systems which extract right and left channel system voice signals as mutual additional signals, and control each signal level, phase control circuits 8R and 8L for the right and left channel systems which control the phases of the signals which are controlled in level by the both attenuators 7R and 7L, and microcomputer 9 which controls the level attenuating amounts of the both attenuators 7R and 7L, and the phase shift amounts of the both phase control circuits 8R and 8L based on listening position information for right and left channel system speakers 1R and 1L. Then, a signal phase- controlled by the phase control circuit 8R for the right channel system is inverted in phase, and added to the left channel system voice signal, and the signal phase-controlled by the phase control circuit 8L for the left channel system is inverted in phase, and added to the right channel system voice signal.

COPYRIGHT: (C)1994,JPO

